SW1
SpecialReset
R3
10k
R2
1k
C1
1µ
U3A
74HC14
U2A
74HC74
U2B
74HC74
U5A
74HC00
D2
RESET
SRESET
sync with M1
CLK
U3B
74HC14
PM1
PM1
U1
TL7705A
R1
10k
PWR_RES
C2
22µ
C3
100n
U21
ATmega32A-P
RESET
XTAL1
XTAL2
AREF
PA0 V0
PA1 V1
PA2 V2
PA3 V3
PA4 V4
PA5 V5
PA6 V6
PA7 V7
PB0 SRCLK
PB1 SER
PB2 RCLK
PB3 64x16
PB4 S5
PB5 R5
PB6 R6
PB7 V8
PC0 S0
PC1 S1
PC2 S2
PC3 DoubleScan
PC4 WR
PC5 E
PC6 RS
PC7 CE
PD0 D0
PD1 D1
PD2 D2
PD3 D3
PD4 D4
PD5 D5
PD6 D6
PD7 D7
XCK TO T1
INT2 AIN0
OCO AIN1
SS
MOSI
MISO
SCK
RXD
TXD
R[0..6]
S[0..2]
V[0..8]
D[0..8]
X2
16MHz
U16
ICS525-01R
X1 ICLK
X2
R0
R1
R2
R3
R4
R5
R6
S0
S1
S2
V0
V1
V2
V3
V4
V5
V6
V7
V8
PD
CLK
REF
PXLCK
U20
74LS163
DISP_EN
CHRCLK2
U16
74LS138
LD
U18
74LS166
SER_PX
R4
150
U13
LM385Z-ADJ
1k1
R_Potentiometer
U9D
74HC03
U9C
74HC03
U12A
74HC03
U9B
74HC03
R5
22
R6
390
J1
Conn_Coaxial
U14
74LS175
HSYNC
VSYNC
PXLCK
+HSYNC
-HSYNC
+VSYNC
-VSYNC
J2
DB9_Female
J3
DB15_Female_HighDensity
RV1
1kΩ
ID0
ID1
HS+/-
VS+/-
ID3
KEY+5V
ID2
JP2
Jumper_3_Open
JP3
Jumper_3_Open
U11
MC6845
MA0..MA10
2048x2 characters=4096 characters total
U28
74LS595
SER
SRCLK
SRCLR
RCLK
OE
U7
74HC138
U6A
74HC10
D1
DX13
MEMDI
EDOO-FFFF
MREQ
U27
74LS273
PX0
PX1
FZG
ZGbnk
64x16
U29
74LS175
Line0
Line1
Line2
TEXT_MODE
GFX_MODE
U25A
74HC139
U26A
74HC02
IOSEL.04
U25B
74HC139
U26B
74HC02
IOSEL.18
U4
74HC138
IO00
IO10
IO70
IO30
IO40
IO50
IO60
IO70
U24
74LS257
VrA0
VrA1
VrA2
VrA3
RA0 scanline or RA4
DoubleScan
VrA[0..3]
VrA3 is blank pixel signal
U17
KM681000BLP
VID_A15
VID_A16
VID_WR
CS2
NC
U15
28C256
Z[0..7]
JP WE A14
U8
74HC245
DG[0..7]
U30
74LS244
GFX_MODE
U31
74LS257
VID_A12
VID_A13
VID_A14
VID_A11
U23
74LS257
VID_A00
VID_WR
VID_A01
VID_A02
U22
74LS257
VID_A03
VID_A04
VID_A05
VID_A06
U19
74LS257
VID_A07
VID_A08
VID_A09
VID_A10
CPU address or latch port 4/18
U2C
74HC74
U9E
74HC03
U25C
74HC139
U26E
74HC02
C11
100n
LOET.DE
Sheet: /
File: Z1013 Development MC6845.kicad_sch
Title: Sarvahari Grafikkarte HGC CGA VGA PAL
Size: A2
Date: 2024-05-20
Rev: 1
KiCad E.D.A. kicad (6.99.0-3013-g0070cbd2ad)
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